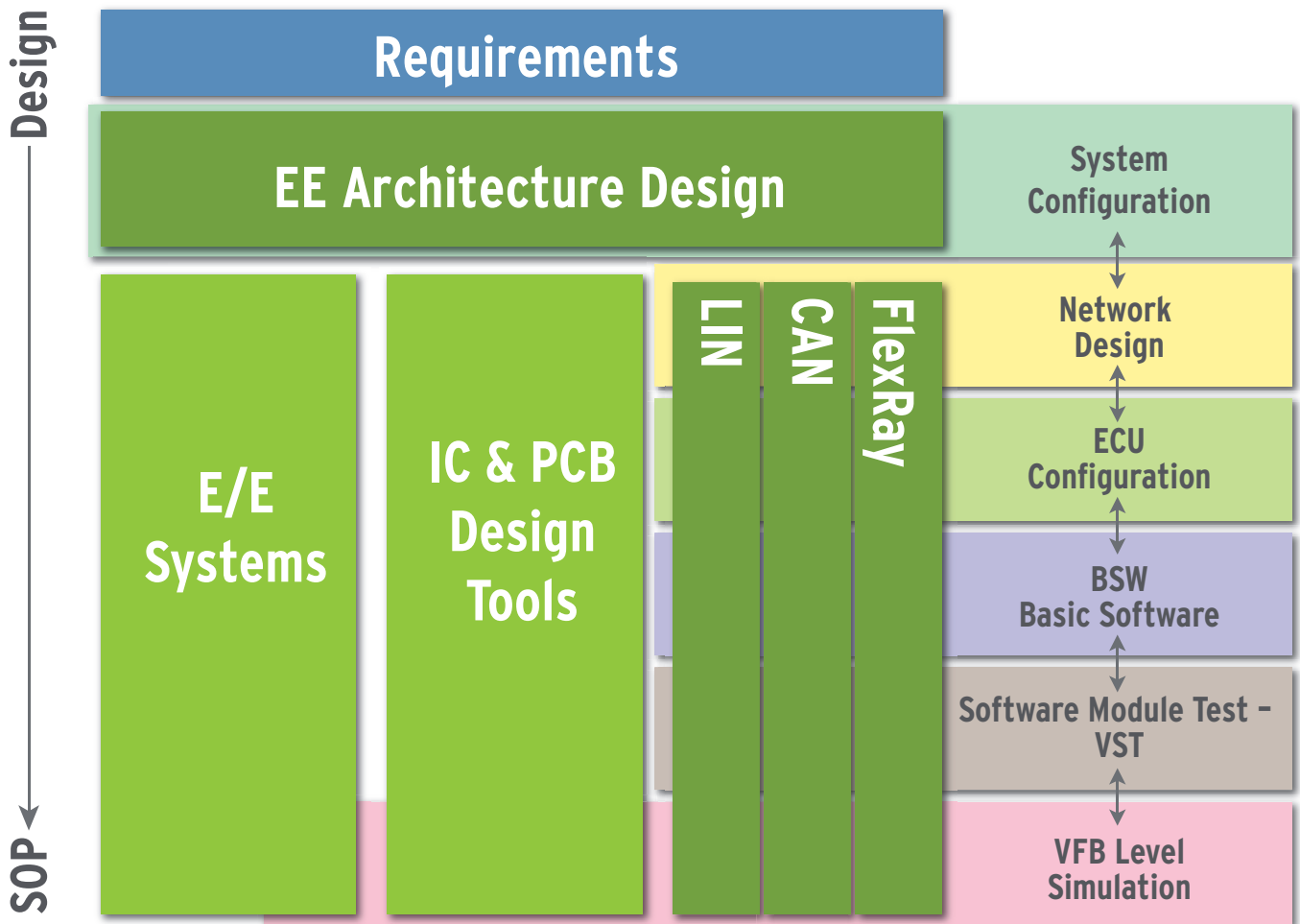


## E/E Design Flow Solution for **AUTOSAR** and beyond



### ECU Configuration - BSW

- Configuration of AR BSW
- Guided Configuration
- Consistency Checks
- Use-case specific Profiles

### System Configuration - VSA

- Architecture Design
- Vehicle Function Creation
- Topology Definition
- Bus Scheduling
- Initial ECU Scheduling
- Metrics Generation

### Network Design - LIN/CAN/ FlexRay Cluster Builder

- Automatic Signal2Frame Packing
- Automatic Gateway Compilation
- Bus Timing Model/Analysis
- „Design to Correctness“

### Basic Software

- Codesize-/Performance optimized
- MISRA-compliant

### VFB<sup>1</sup> Level Simulation - VSI

- Debugging and Profiling
- Virtual Integration
- Verification across single, as well as multiple ECUs

<sup>1</sup>VFB = Virtual Functional Bus

### Software Module Test - VST

- TTCN3
- Support for AUTOSAR conformance tests

